

CAST [1071X10MM and with borders 3.333x3.333]											
[File] [Edit] [List] [Window] [Help]											
[New] [Open] [Save] [Print] [Exit]											
<p>↳ Drafts</p> <ul style="list-style-type: none"> ↳ BRS: ↳ BRS: ↳ BRS: ↳ BRS: knall ↳ BRS: 19 21 23 ↳ BRS: 32with 33 <p>↳ Pending</p> <p>↳ Active</p> <ul style="list-style-type: none"> ↳ L1: (0) 10/713510 ↳ L2: (1) 10/165510 ↳ L3: (719429) (((ferr?magnetic ferr?adj magnetic) ((magnetic) (magn...)) ↳ L4: (940632) (sidewall side adj (wall surface)) ↳ L5: (3154405) conductive conductor electrode contact ↳ L6: (26355) L4 near L5 ↳ L7: (719878) memory ↳ L8: (548) L6 with L7 ↳ L9: (84) L8 and L3 <p>↳ Failed</p> <p>↳ Saved</p> <ul style="list-style-type: none"> ↳ S2: (1) 09/948877 ↳ S3: (1736) "KONINKLIJKE PHILIPS".as. ↳ S4: (0) 10/698514 ↳ S5: (0) 10/705504 											
<p>L8 and L3</p> <p>Feb, 2005</p>											
US	Inventor	Document Issue P	Title	Current Status	Current X	Retrieval	S	C	P	Image	DoP
1	<input type="checkbox"/>	Lowrey, Ty	US 675489 2004 4	Method of making programmable resistors	438/23	438/381	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 67648	
2	<input type="checkbox"/>	Nejad, Has	US 200500 2005 7	Methods of forming magnetoresistive	438/3		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050	
3	<input type="checkbox"/>	Malimon, Jo	US 200500 2005 2	Programmable resistance memory ele	257/2		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050	
4	<input type="checkbox"/>	Lowrey, Ty	US 200500 2005 4	Programmable resistance memory ele	438/20		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050	
5	<input checked="" type="checkbox"/>	Anthony, T	US 200402 2004 9	Magnetic memory	365/17		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	
6	<input type="checkbox"/>	Kostylev, S	US 200402 2004 4	Electrically programmable memory ele	257/53		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	
7	<input type="checkbox"/>	Lowrey, Ty	US 200402 2004 3	Electrically programmable memory ele	257/53		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	
8	<input type="checkbox"/>	Kim, Eun-Si	US 200402 2004 1	High density magnetoresistance mem	365/17	365/171	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	
9	<input type="checkbox"/>	Hsu, Sheng	US 200402 2004 2	High-density SOI cross-point memory	438/70		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	
10	<input type="checkbox"/>	Lowrey, Ty	US 200401 2004 6	Method for making programmable resi	438/85	257/E27.0	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040	